

CONTENTS

<i>Sławomir Cichoń, Marek Gorgoń</i> FPGA-based DVCPRO HD decoder implementation using Impulse C	531
<i>Tomasz Jurczyk, Barbara Glut</i> Preparation of control space for remeshing polygonal surfaces	547
<i>Marek Kisiel-Dorohinicki</i> Evolutionary multi-agent systems in non-stationary environments	563
<i>Rafał Leszko, Kamil Piętak</i> Verification mechanism for lightweight component-based environment based on IoC container	577
<i>Krzysztof Kuźnik, Maciej Paszyński, Victor Calo</i> Grammar based multi-frontal solver for isogeometric analysis in 1D.	589
<i>Maria Magdoń-Maksymowicz, Andrzej Maksymowicz</i> On the evolution of migrating population with two competing species	615
<i>Ewa Gajda-Zagórska, Maciej Paszyński, Robert Schaefer, David Pardo</i> hp-HGS strategy for inverse AC/DC resistivity logging measurement simulations	629
<i>Leszek Siwik, Kamil Włodarczyk, Mateusz Kluczny</i> Staged event-driven architecture as a micro-architecture of distributed and pluginable crawling platform	645
<i>Tomasz Szydło, Paweł Suder, Jakub Bibro</i> Message-oriented communication for IPv6-enabled pervasive devices	667
<i>Dominik Żurek, Marcin Pietroń, Maciej Wielgosz, Kazimierz Wiatr</i> The comparison of parallel sorting algorithms implemented on different hardware platforms	679